

FORM PTO-892		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		SERIAL NO. 09/556,472	GROUP ART UNIT 2676	ATTACHMENT TO PAPER NO.	3
NOTICE OF REFERENCES CITED				APPLICANT(S) Mang et al			
U.S. PATENT DOCUMENTS							
*		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	A	6,298,364	10/2001	Kanekura	708	490	
	B	5,119,324	6/1992	Ahsan	708	520	
	C	6,085,213	7/2000	Oberman et al.	708	603	
	D	5,838,463	11/1998	Gahang	358	465	
	E	4,996,660	2/1991	Beacom et al.	708	504	
	F						
	G						
	H						
	I						
	J						
	K						
FOREIGN PATENT DOCUMENTS							
*		DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUB- CLASS
	L						
	M						
	N						
	O						
	P						
	Q						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)							
	R	High Speed Primitives of Hardware Accelerators DSP In GaAs Technology (IEEE Proceedings-G, No. 2, April 1992) by Sarmiento et al					
	S	Design of Synchronous and Asynchronous variable -Latency Pipelined multipliers (IEEE transactions on Very Large Scale Integration Vol. 9 No. 2, April 1992) by Mauro Olivieri					
	T						
	U						
EXAMINER			DATE				
Mackly Monestime			October 22, 2002				
Form892ccs2106b							
* A copy of this reference is not being furnished with this office action. (See Manual of Patent Examining Procedure, section 707.05(a).)							